CLAIM AMENDMENTS

Claim Amendment Summary

Claims pending

Before this Amendment: Claims 1-45.

• After this Amendment: Claims 1-22

Non-Elected, Canceled, or Withdrawn claims: 23-45

Amended claims: none

New claims: none

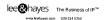
Claims:

1. (Original) A processor-readable medium having processor-executable instructions that, when executed by a processor, performs a method comprising:

determining where a dynamic embedded-signal detection program module ("detector") receives a subject input stream for the detector to perform detection thereon to determine if the stream has an embedded-signal therein;

interfering with clear reception of the subject input stream, thereby hindering detection by the detector.

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2. (Original) A medium as recited in claim 1 further comprising

observing the detector in a processor-readable memory of a computer to

determine its location in such memory.

3. (**Original**) A medium as recited in claim 1, wherein the interfering

comprises adjusting "play-rate" of the incoming stream.

4. (Original) A medium as recited in claim 1, wherein the interfering

comprises introducing a countersignal into the incoming stream.

5. (Original) A medium as recited in claim 1, wherein the interfering

comprises introducing noise into the incoming stream.

6. (Original) A medium as recited in claim 1 further comprising

maintaining the interfering while the input stream is being consumed.

7. (Original) A medium as recited in claim 1, wherein the type of the

subject input stream is selected from a group consisting of image, audio, video,

multimedia, software, metadata, and data.

8. (Original) A computing device comprising:

an input device for receiving one or more input streams;

a medium as recited in claim 1.

(Original) A method facilitating circumvention of dynamic, robust,

embedded-signal detection, the method comprising:

determining where a dynamic embedded-signal detection program module

("detector") receives a subject input stream for the detector to perform detection

thereon to determine if the stream has an embedded-signal therein;

interfering with clear reception of the subject input stream, thereby

hindering detection by the detector.

10. (Original) A method as recited in claim 9 further comprising

observing the detector in a processor-readable memory of a computer to

determine its location in such memory.

11. (Original) A method as recited in claim 9 wherein the interfering

comprises adjusting "play-rate" of the incoming stream.

Serial No.: 10/676,499 Atty Docket No.: MS1-1349US Atty/Agent: Kasey C. Christie **12.** (Original) A method as recited in claim 9, wherein the interfering

comprises introducing a countersignal into the incoming stream.

13. (Original) A method as recited in claim 9, wherein the interfering

comprises introducing noise into the incoming stream.

14. (Original) A method as recited in claim 9 further comprising

maintaining the interfering while the input stream is being consumed.

15. (Original) A method as recited in claim 9, wherein the type of the

subject input stream is selected from a group consisting of image, audio, video,

multimedia, software, metadata, and data.

16. (Original) A computing device comprising one or more processor-

readable media having processor-executable instructions that, when executed by

the computer, perform the method as recited in claim 9.

17. (Original) A system facilitating circumvention of dynamic, robust,

embedded-signal detection, the system comprising:

a memory-location determiner configured to determine where a dynamic

embedded-signal detection program module ("detector") receives a subject input

stream for the detector to perform detection thereon to determine if the stream

has an embedded-signal therein;

an interferer configured to interfere with clear reception of the subject

input stream, thereby hindering detection by the detector.

18. (Original) A system as recited in claim 17, wherein the memory-

location determiner is further configured to observe the detector in a processor-

readable memory of a computer to determine its location in such memory.

19. (Original) A system as recited in claim 17, wherein the interfering

comprises adjusting "play-rate" of the incoming stream.

20. (Original) A system as recited in claim 17, wherein the interferer

is further configured to introduce a countersignal into the incoming stream.

21. (Original) A system as recited in claim 17, wherein the interferer

is further configured to introduce noise into the incoming stream.

22. (Original) A system as recited in claim 17, wherein the type of the subject input stream is selected from a group consisting of image, audio, video, multimedia, software, metadata, and data.

23-45. (Canceled)